

ENGINEERING  
**DataXpress**

## ***DXL1002 Cadence Composer Schematic Translator***

The DXL1002™ Cadence Composer Schematic Translator is one of a series of optimized data links that is part of the DataXpress Integrator™ product line. The DataXpress Integrator was developed as a solution to the problem of moving design data from one EDA vendor's tools to another's.

The DXL1002 schematic translator allows Cadence Composer users to translate schematic design data from the Engineering DataXpress database called EDI into Composer. EDIF schematic files can be read into the EDI database and translated into the Cadence database.

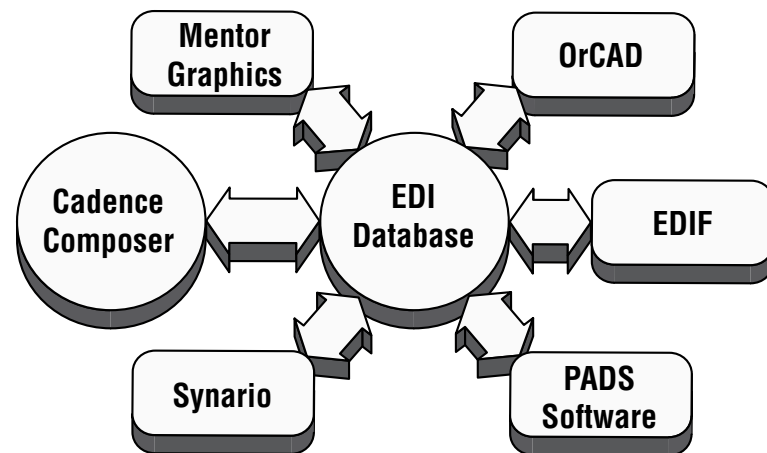
The DXL1002 can also be combined with other Engineering DataXpress translators for direct database translation into Composer from the EDA vendors that we currently support. The list of translators that we offer includes OrCAD, Synario, PADS Software, Mentor Graphics, Viewlogic and others.

Schematics and cell libraries can be transferred independently, or a schematic and its library of subcomponents can be transferred at the same time. The full design hierarchy can be transferred or it can be limited in various ways. Also, when reading an EDIF file, schematics are allowed to reference symbols which are defined externally. Options and commands for the translator are specified on the command line or in a configuration file.

The DXL1002 Schematic Translator is fully supported by Engineering DataXpress, a worldwide leader in data translation technology. This support ensures that the translator will continue to be enhanced with new features and options and will remain current with each new release of Cadence Composer Software.

### ***Features***

- Translation into Cadence Composer schematics and symbols from other systems.
- Can translate connectivity by name, busses, and comment graphics.
- User control over name mapping, grids, library transfer, etc.
- Handles hierarchy in Cadence schematics, symbols, and EDIF cells.



**DataXpress Integrator™**

## Product Description

The DXL1002 schematic translator transfers schematic and symbol information into Cadence Composer through the EDI database. This data is transferred directly from other EDA vendors who are also linked to the EDI database or from any EDA vendor that has an EDIF schematic interface.

The DXL1002 takes an EDIF input file or EDI database, and creates Cadence schematics and/or symbols which can then be edited with Composer. The reader can read and translate any correct EDIF level 0 schematic representation into Composer constructs.

Cadence specific elements such as connectivity by name, both between sheets and to members of busses, off-page connectors, net connectors and graphics are all translated from their equivalent EDIF representations.

The translator uses commands from the command line and/or configuration files to determine user options and to locate files in a design hierarchy. Using a configuration file, the user can specify globally for the entire translation or within a specific library or cell how various properties and attributes are to be mapped from the EDIF file.

By default, a complete design hierarchy is translated. Simple commands can limit the traversal of the hierarchy to only those schematics and symbols which are to be translated.

Using configuration files and/or command line options, the user can specify the following to control the translation process:

- Which schematics, symbols and cell libraries to translate.
- Scaling of sheets and symbols.
- Pin Grid matching.
- Name mapping by character, substring, prefix and suffix for nets, symbols, instances, properties and library names.
- How properties map from EDIF properties.
- How properties map from EDIF attributes.
- Translation of cell libraries.
- Translation of design hierarchy.
- Overwriting of schematics and symbols.

## Supported Platforms

- Sun4/SunOS 4.1.4+
- Sun4/SunOS 5.x (Solaris 2.x).
- HP9000/7xx HP-UX 9.xx or greater.

## Software Requirements

- Cadence Composer software release 4.3.4 or later.
- EDIF version 2 0 0.

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